

Abstract

Transformer Circuit Arrangement

5 A transformer circuit arrangement has a first transformer (201) with a first lower limit frequency (f_{l1}) and a first upper limit frequency (f_{u1}), and a second transformer (202) with a second lower limit frequency (f_{l2}) and a second upper limit frequency (f_{u2}), whereby the first lower limit
10 frequency (f_{l1}) is smaller than the second upper limit frequency (f_{u2}), and the second upper limit frequency (f_{u2}) is greater than the first upper limit frequency (f_{u1}), the second lower limit frequency (f_{l2}) is preferably not greater or smaller than the first upper limit frequency (f_{u1}) by a
15 factor of 10, at least one input (103, 104) of the first transformer is electrically connected to an input (209, 210) of the second transformer, and at least one output (205, 206) of the first transformer is connected to an output (107, 108) of the second transformer, and the transformer
20 circuit arrangement has a band pass behaviour with a lower overall limit frequency (f_{lges}) and an upper overall limit frequency (f_{uges}), whereby the lower overall limit frequency (f_{lges}) is smaller than the first upper limit frequency (f_{u1}) and the second lower limit frequency (f_{l2}), and
25 whereby the upper overall limit frequency (f_{uges}) is greater than the first upper limit frequency (f_{u1}) and the second upper limit frequency (f_{u2}).

(Fig. 4)